

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned

Examiner: To Be Assigned

In re PATENT APPLICATION of:

Applicant(s) : Toshikazu MIZUKOSHI)

Appln. No. : To Be Assigned)

Filed : August 7, 2003)

For : METHOD OF FABRICATING A)
SEMICONDUCTOR DEVICE HAVING)
TRENCHES)

Atty. Dkt. : OKI 361)

**INFORMATION
DISCLOSURE
STATEMENT**

August 7, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

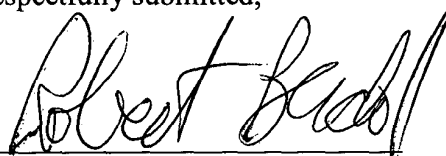
Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached are copies of four Japanese publications and one article. Any relevance of the Japanese publications or article can be gleaned from the attached English-language Abstracts or translations. The publications and article are listed on the attached Form PTO-1449. Also listed on the Form PTO-1449 for consideration are three U.S. patents. Any relevance of these U.S. patents is self-evident.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the document submitted. Consideration of the submitted and listed documents is respectfully requested.

Respectfully submitted,



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August 7, 2003
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RHB:tz

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty Docket		Application No.	
				OKI 361		To Be Assigned	
				Applicant			
				Toshikazu MIZUKOSHI			
				Filing Date		Group Unit	
				August 7, 2003		To Be Assigned	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
	AA	6,268,629	07/31/01	Noguchi			
	AB	6,245,684	06/12/01	Zhao et al.			
	AC	6,051,479	04/18/00	Hong			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub-Class	Translation
	AK	2001-244327	09/07/01	Japan			Abstract
	AL	2000-101071	04/07/00	Japan			Abstract
	AM	11-102961	04/13/99	Japan			Abstract
	AN	10-223747	08/21/98	Japan			Abstract
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
	AO	1991.3, Semiconductor World, p128-p131, and translation thereof.					
	AP						
	AQ						
	AR						
Examiner					Date Considered		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							